



## CE-ATA Technical Errata

Errata ID	Protocol 010
Affected Spec Ver.	Protocol 1.0
Corrected Spec Ver.	

### Submission info

Name	Company	Date
Amber Huffman	Intel	07/20/2005

### Description of the specification technical flaw (add space as needed)

The definition table in the Timing Requirements section has an incorrect definition for CRC16. CRC16 is 16-bits in MMC, not 15-bits as currently marked. The erratum corrects this error.

### Description of the correction

***The definitions table in the Timing Requirements section, section 3, shall be modified as shown:***

CRC16	CRC check bits ( <del>15-bit</del> 16-bit)
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### Disposition log

07/20/2005	Erratum captured
08/31/2005	Erratum ratified

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