



## CE-ATA Technical Errata

Errata ID	Protocol 005
Affected Spec Ver.	Protocol 1.0
Corrected Spec Ver.	

### Submission info

Name	Company	Date
Amber Huffman	Intel	05/10/2005

### Description of the specification technical flaw (add space as needed)

The DC\_CmdChkType state defines the MMC commands that a device supports. This state does not currently allow device support for additional MMC commands. This erratum allows devices to support additional MMC commands and references the MMC specification for their behavior.

### Description of the correction

***The DC\_CmdChkType state in section 2.4.2.1 shall be updated as shown:***

DC6: DC_CmdChkType	Determine if the command received is a valid MMC command.		
1. RW_MULTIPLE_REGISTER (CMD60) received or RW_MULTIPLE_BLOCK (CMD61) received	→	DC_Cmd6X_Entry	
2. FAST_IO (CMD39) received	→	DC_Cmd39_Entry	
3. STOP_TRANSMISSION (CMD12) received	→	DC_Cmd12_Entry	
4. Command index supported by the device received that is not equal to 0, 12, 39, 60, or 61	→	Refer to MMC <sup>2</sup>	
5. Command index not supported by the device received equal to 12, 39, 60, or 61	→	DC_Idle <sup>1</sup>	
NOTE:			
1. ATA layer is notified that an illegal MMC command was received.			
2. The behavior for supported MMC command index values other than 0, 12, 39, 60, and 61 is not defined within this specification. For these command index values, the MMC reference defines the appropriate behavior.			

### Disposition log

05/10/2005	Erratum captured
05/25/2005	Removed requirement to enter DC_Idle state after command completion in note 2.
07/11/2005	Erratum ratified

*Technical input submitted to the CE-ATA Workgroup is subject to the terms of the CE-ATA contributor's agreement.*