



## CE-ATA Technical Errata

<b>Errata ID</b>	<b>Protocol 017</b>
<b>Affected Spec Ver.</b>	<b>Protocol 1.0</b>
<b>Corrected Spec Ver.</b>	

### Submission info

Name	Company	Date
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### Description of the specification technical flaw (add space as needed)

The T13 ATA specification uses the upper 4 bits of Word 106 in IDENTIFY DEVICE for particular purposes. These 4 bits are defined as part of the CE-ATA sector size definition. However, these bits do not need to be part of the CE-ATA sector size definition since sector sizes will never reach  $2^{4096}$  bytes in size. To avoid any issues with solutions that support CF+ in addition to CE-ATA and to avoid any potential for confusion, this erratum marks these bits as reserved for CE-ATA.

## Description of the correction

**Word 106 of the IDENTIFY DEVICE data structure defined in section 4.2.1.4 shall be modified as shown:**

106	M	F	CE-ATA sector size 15-12 reserved 11-0 CE-ATA sector size
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**Section 4.2.1.10 shall be modified as shown:**

### **Word 106: CE-ATA sector size**

~~Word 106~~ Bits 11-0 indicates the CE-ATA sector size. All device media accesses shall be in full units of the device's reported CE-ATA sector size and shall be aligned on boundaries an integral multiple of the reported CE-ATA sector size. IDENTIFY DEVICE has a fixed data size of 512 bytes. ~~The~~ This value ~~in word 106~~ is reported in terms of a power of 2. For instance, a reported value of 14 corresponds to a CE-ATA sector size of  $2^{14}$  or 16384 bytes. A value smaller than 12 (i.e. 4096 bytes) is not supported.

Bits 15-12 are reserved.

## Disposition log

10/14/2005	Erratum captured
1/12/2006	Erratum ratified

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