



## CE-ATA Technical Errata

Errata ID	Protocol 007
Affected Spec Ver.	Protocol 1.0
Corrected Spec Ver.	

### Submission info

Name	Company	Date
Amber Huffman	Intel	05/29/2005

### Description of the specification technical flaw (add space as needed)

The CE-ATA 1.0 protocol state machine implies that it is mandatory that a drive support issuing an ATA command by the host writing the ATA Command register with FAST\_IO (CMD39). This erratum clarifies that CE-ATA 1.0 devices do not support this capability.

### Description of the correction

***The DC\_Cmd39\_WriteReg state in section 2.4.2.1.2 shall be modified as shown:***

DC14: DC_Cmd39_WriteReg <sup>1</sup>		Write the Register Data specified to the Register Address specified in FAST_IO (CMD39).	
	1. Register contents have been written	→	DC_Cmd39_R4
	2. Register contents have not been written	→	DC_Cmd39_WriteReg
	<b>NOTE:</b> 1. Writing the ATA Command register with FAST IO (CMD39) is not supported.		

### Disposition log

05/29/2005	Erratum captured
06/13/2005	Changed "not defined" to "not supported" per 6/1 meeting
07/27/2005	Erratum ratified

*Technical input submitted to the CE-ATA Workgroup is subject to the terms of the CE-ATA contributor's agreement.*